

Digital AI Hardware Advances

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Bio:

Sunil Shukla is a Research Staff Member and a Manager at the IBM T.J. Watson Research Center in Yorktown Heights, NY, where he leads an architecture and logic design team. Over the past decade at IBM Research, he has worked on projects ranging from hardware accelerators to programming languages and runtimes. He is currently leading the development of an accelerator system for Deep Learning. He has a Ph.D. in Electrical Engineering from the University of Queensland, Australia.

Abstract:

In this talk, Sunil will describe the lessons learned from designing multiple generations of deep learning accelerators for edge devices to datacenters. This talk will build upon the previous talk on precision scaling. He will talk about the evolution of the digital AI core which exploits the algorithmic advances.